## **Claims**

## What is claimed is:

- 1. A semiconductor device comprising:
- 5 at least two magnetic layers; and

a spacer layer formed between the magnetic layers, the spacer layer being configured to provide ferromagnetic exchange coupling between the layers, the magnetic layers experiencing anti-ferromagnetic dipole coupling, such that a net coupling of the magnetic layers is anti-ferromagnetic in a zero applied magnetic field.

- 10 2. The device of claim 1, comprising magnetic random access memory (MRAM).
  - 3. The device of claim 1, wherein the net coupling of the magnetic layers comprises a sum of the exchange coupling and the dipole coupling.
- 4. The device of claim 1, wherein each of the magnetic layers has a same 15 thickness.
  - 5. The device of claim 1, wherein a difference in a thickness of each of the magnetic layers relative to one another is less than or equal to about ten percent.
    - 6. The device of claim 1, wherein the magnetic layers are elliptical.
    - 7. The device of claim 1, wherein the magnetic layers are circular.

- 8. The device of claim 1, wherein each of the magnetic layers has a same intrinsic anisotropy.
- 9. The device of claim 1, wherein one or more of the magnetic layers comprises an element selected from the group consisting of nickel, cobalt, iron, manganese and combinations comprising at least one of the foregoing elements.
- 10. The device of claim 1, wherein one or more of the magnetic layers comprise Ni<sub>80</sub>Fe<sub>20</sub>.
- 11. The device of claim 1, wherein the spacer layer comprises a transition metal.
- 10 12. The device of claim 11, wherein the transition metal is selected from the group consisting of chromium, copper, ruthenium, rhodium, palladium, rhenium, osmium, iridium, platinum and combinations comprising at least one of the foregoing transition metals.
- 13. The device of claim 1, wherein the spacer layer comprises an incomplete layer.
  - 14. The device of claim 1, wherein the spacer layer comprises pinholes.
  - 15. The device of claim 1, wherein the spacer layer comprises an insulator.
  - 16. The device of claim 1, wherein the spacer layer comprises aluminum oxide.

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- 17. The device of claim 1, wherein the spacer layer is non-magnetic.
- 18. The device of claim 1, wherein the spacer layer comprises a weak ferromagnet.
- 19. The device of claim 18, wherein the weak ferromagnet comprises magnesium, iron, cobalt, nickel and combinations comprising at least one of the foregoing elements.
  - 20. The device of claim 1, wherein the spacer layer has a thickness of from about one nanometer to about 1.6 nanometers.
- The device of claim 1, wherein the spacer layer has a thickness of from about two nanometers to about 2.8 nanometers.
  - 22. A method for coupling magnetic layers in a semiconductor device comprising at least two magnetic layers and a spacer layer therebetween, the method comprising the step of providing ferromagnetic exchange coupling of the magnetic layers, the magnetic layers experiencing anti-ferromagnetic dipole coupling, such that a net coupling of the magnetic layers is anti-ferromagnetic in a zero applied magnetic field.
  - 23. The method of claim 22, wherein the spacer layer comprises a transition metal.
  - 24. The method of claim 23, wherein the transition metal is selected from the group consisting of chromium, copper, ruthenium, rhodium, palladium, rhenium,

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osmium, iridium, platinum and combinations comprising at least one of the foregoing transition metals.

25. The method of claim 22, further comprising the step of varying a thickness of the spacer layer to attain ferromagnetic exchange coupling of the magnetic layers.

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